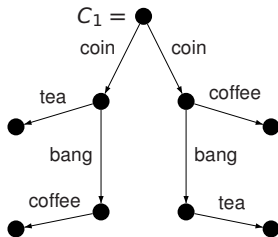


Introduction to formal methods

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- The method of algorithm verification discussed earlier needs access to the code (**white box**)
- Formal methods do not assume that code is available (**black box**)
 - We ignore everything in terms of internal functionality of the system
 - At any given time the system is in one of the many **states** from a given set S
 - Alternate definition: Set V of variables, each state is an interpretation over V
 - A finite / countable set of states given us a finite automaton / transition system
 - We change state by performing one **action** from a set of actions L
 - Only certain actions are enabled in each state
 - **Special action: unobservable** (τ)
 - Systems are considered **reactive** (react to the environment)



$C_1 =$
 coin \rightarrow
 (tea \rightarrow STOP \square
 bang \rightarrow coffee \rightarrow STOP)
 \square
 coin \rightarrow
 (coffee \rightarrow STOP \square
 bang \rightarrow tea \rightarrow STOP)



- System specification uses a **process algebra** = identifies the states and actions
 - Compositional specification (start from simple “processes” and combine them to obtain increasingly complex ones)
 - Descriptions compilable into state transition diagrams / transition systems
- Such a process algebra is **CSP**:
 - Simplest process: **STOP** \Rightarrow does not perform anything
 - Perform an action: $R = a \rightarrow P \Rightarrow R$ can perform a and then becomes P

$\text{PRINTER} = \text{accept} \rightarrow \text{print} \rightarrow \text{STOP}$

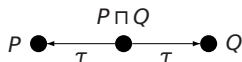
- Choice: $R = P \sqcap Q \Rightarrow R$ becomes either P or Q depending on what action is offered by the environment

$\text{PRINTER} = (\text{accept} \rightarrow \text{print} \rightarrow \text{STOP}) \sqcap (\text{shutdown} \rightarrow \text{STOP})$

- **Recursion**: some action gets us back into a process (or state) already named

$\text{PRINTER} = (\text{accept} \rightarrow \text{print} \rightarrow \text{PRINTER}) \sqcap (\text{shutdown} \rightarrow \text{STOP})$

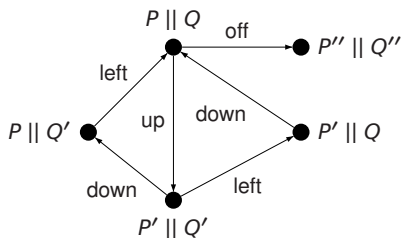
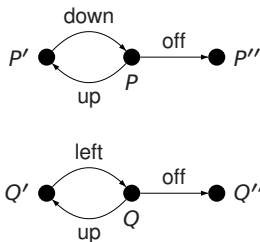
- Internal choice: $R = P \sqcap Q \Rightarrow R$ first becomes either P or Q “spontaneously” before any interaction with the environment:



PRINTER = (accept \rightarrow print \rightarrow STOP) \sqcap (shutdown \rightarrow STOP)

- Parallel composition: $R = P \parallel Q \Rightarrow P$ and Q execute common actions synchronized and individual actions separately

$P \parallel Q$, where: $P = \text{up} \rightarrow \text{down} \rightarrow P \sqcap \text{off} \rightarrow \text{STOP}$
 $Q = \text{up} \rightarrow \text{left} \rightarrow Q \sqcap \text{off} \rightarrow \text{STOP}$





ISABELLA = isabella.get.box \rightarrow isabella.get.easel \rightarrow isabella.paint \rightarrow
isabella.drop.box \rightarrow isabella.drop.easel \rightarrow ISABELLA
 \square isabella.get.easel \rightarrow isabella.get.box \rightarrow isabella.paint \rightarrow
isabella.drop.box \rightarrow isabella.drop.easel \rightarrow ISABELLA

KATE = kate.get.box \rightarrow kate.get.easel \rightarrow kate.paint \rightarrow
kate.drop.box \rightarrow kate.drop.easel \rightarrow KATE
 \square kate.get.easel \rightarrow kate.get.box \rightarrow kate.paint \rightarrow
kate.drop.box \rightarrow kate.drop.easel \rightarrow KATE

EASEL = isabella.get.easel \rightarrow isabella.drop.easel \rightarrow EASEL
 \square kate.get.easel \rightarrow kate.drop.easel \rightarrow EASEL

BOX = isabella.get.box \rightarrow isabella.drop.box \rightarrow BOX
 \square kate.get.box \rightarrow kate.drop.box \rightarrow BOX

PAINTING = ISABELLA || KATE || EASEL || BOX

CSP EXAMPLE: KIDS PAINTING (CONT'D)



$$\begin{aligned} I &= i.g.b \xrightarrow{I1} i.g.e \xrightarrow{I2} i.p \xrightarrow{I3} i.d.b \xrightarrow{I4} i.d.e \rightarrow I \\ &\quad \square \quad i.g.e \xrightarrow{I5} i.g.b \xrightarrow{I6} i.p \xrightarrow{I7} i.d.b \xrightarrow{I8} i.d.e \rightarrow I \\ K &= k.g.b \xrightarrow{K1} k.g.e \xrightarrow{K2} k.p \xrightarrow{K3} k.d.b \xrightarrow{K4} k.d.e \rightarrow K \\ &\quad \square \quad k.g.e \xrightarrow{K5} k.g.b \xrightarrow{K6} k.p \xrightarrow{K7} k.d.b \xrightarrow{K8} k.d.e \rightarrow K \\ E &= i.g.e \xrightarrow{E1} i.d.e \rightarrow E \quad \square \quad k.g.e \xrightarrow{E2} k.d.e \rightarrow E \\ B &= i.g.b \xrightarrow{B1} i.d.b \rightarrow B \quad \square \quad k.g.b \xrightarrow{B2} k.d.b \rightarrow B \end{aligned}$$

$$PAINTING = I \parallel K \parallel E \parallel B$$

$$\begin{array}{lcl}
 I & = & i.g.b \xrightarrow{I1} i.g.e \xrightarrow{I2} i.p \xrightarrow{I3} i.d.b \xrightarrow{I4} i.d.e \rightarrow I \\
 & \square & i.g.e \xrightarrow{I5} i.g.b \xrightarrow{I6} i.p \xrightarrow{I7} i.d.b \xrightarrow{I8} i.d.e \rightarrow I \\
 K & = & k.g.b \xrightarrow{K1} k.g.e \xrightarrow{K2} k.p \xrightarrow{K3} k.d.b \xrightarrow{K4} k.d.e \rightarrow K \\
 & \square & k.g.e \xrightarrow{K5} k.g.b \xrightarrow{K6} k.p \xrightarrow{K7} k.d.b \xrightarrow{K8} k.d.e \rightarrow K \\
 E & = & i.g.e \xrightarrow{E1} i.d.e \rightarrow E \quad \square \quad k.g.e \xrightarrow{E2} k.d.e \rightarrow E \\
 B & = & i.g.b \xrightarrow{B1} i.d.b \rightarrow B \quad \square \quad k.g.b \xrightarrow{B2} k.d.b \rightarrow B
 \end{array}$$

$$PAINTING = I \parallel K \parallel E \parallel B$$

- The process $I1 \parallel K5 \parallel B1 \parallel E2$ is an example of **deadlock**
 - Two (or more) processes are deadlocked whenever they are able perform actions individually in isolation from each other, but no action is possible when they are combined together in a parallel composition

CSP EXAMPLE: DINING PHILOSOPHERS



- Five philosophers sit at a round table with a bowl of rice in the middle and five chopsticks (one in between each philosopher)
- In order to eat a philosopher must acquire both the adjacent chopsticks first

$\text{PHIL}_i = \text{enter}.i \rightarrow$

$((\text{pick}.i.i \rightarrow \text{pick}.i.((i + 1) \bmod 5) \rightarrow \text{eat}.i$
 $\rightarrow \text{put}.i.i \rightarrow \text{put}.i.((i + 1) \bmod 5) \rightarrow \text{leave}.i \rightarrow \text{PHIL}_i))$

\square

$(\text{pick}.i.((i + 1) \bmod 5) \rightarrow \text{pick}.i.i \rightarrow \text{eat}.i$
 $\rightarrow \text{put}.i.((i + 1) \bmod 5) \rightarrow \text{put}.i.i \rightarrow \text{leave}.i \rightarrow \text{PHIL}_i))$

$\text{PHILS} = \text{PHIL}_0 \parallel \text{PHIL}_1 \parallel \text{PHIL}_2 \parallel \text{PHIL}_3 \parallel \text{PHIL}_4$

$\text{CHOP}_j = \text{pick}.j.j \rightarrow \text{put}.j.j \rightarrow \text{CHOP}_j$

$\square \text{pick}.((j - 1) \bmod 5).j \rightarrow \text{put}.((j - 1) \bmod 5).j \rightarrow \text{CHOP}_j$

$\text{CHOPS} = \text{CHOP}_0 \parallel \text{CHOP}_1 \parallel \text{CHOP}_2 \parallel \text{CHOP}_3 \parallel \text{CHOP}_4$

$\text{COLLEGE} = \text{PHILS} \parallel \text{CHOPS}$



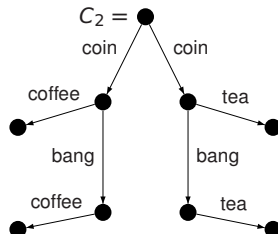
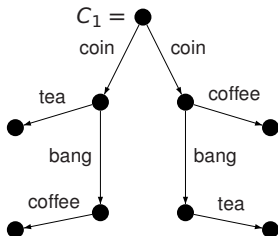
- Black box testing = we do not necessarily have access even to the CSP description
 - Typical scenario: CSP is used to define a specification (intended minimal system behaviour), but the system under test is a true black box
- Comparison between processes must be based on **observable features**
- Simplest observation: **trace** the execution of the process = record the actions of the process as they happen
 - $\text{traces}(\text{accept} \rightarrow \text{print} \rightarrow \text{STOP}) = \{\varepsilon, \langle \text{accept} \rangle, \langle \text{accept}, \text{print} \rangle\}$
 - $\text{PRINT} = \text{accept} \rightarrow \text{print} \rightarrow \text{PRINT}$
 $\Rightarrow \text{traces}(\text{PRINT}) = \langle \text{accept}, \text{print} \rangle^* (\varepsilon + \langle \text{accept} \rangle)$
- Defines an **implementation relation** and associated **equivalence relation**:

$$I \sqsubseteq_T S \quad \text{iff} \quad \text{traces}(S) \subseteq \text{traces}(I)$$

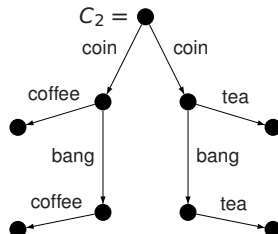
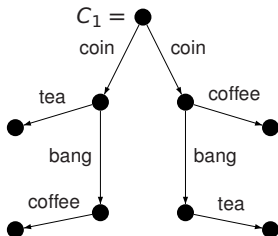
$$I \equiv_T S \quad \text{iff} \quad S \sqsubseteq_T I \ \&\& \ I \sqsubseteq_T S \quad \text{iff} \quad \text{traces}(S) = \text{traces}(I)$$

- Simple verification with traces: Let $C = \text{traces}(S) \cap \overline{\text{traces}(I)}$
 - $I \sqsubseteq_T S$ whenever $C = \emptyset$; otherwise C contains **counterexamples**
 - C is cheap to compute whenever I and S are finite automata

TRACES OF COFFEE MACHINES



- Are C_1 and C_2 equivalent?



- Are C_1 and C_2 equivalent?
- One may argue either way
- Trace-wise we actually have $C_1 \equiv_T C_2$

$$\text{traces}(C_1) = \text{traces}(C_2) = \{ \varepsilon, \langle \text{coin} \rangle, \langle \text{coin}, \text{tea} \rangle, \langle \text{coin}, \text{coffee} \rangle, \\ \langle \text{coin}, \text{bang} \rangle, \langle \text{coin}, \text{bang}, \text{tea} \rangle, \\ \langle \text{coin}, \text{bang}, \text{coffee} \rangle \}$$

IS TRACE PREORDER FINE ENOUGH?


$$X = a \rightarrow b \rightarrow \text{STOP} \sqcap b \rightarrow a \rightarrow \text{STOP}$$
$$Y = a \rightarrow b \rightarrow \text{STOP} \sqcap b \rightarrow a \rightarrow \text{STOP}$$
$$P = a \rightarrow b \rightarrow \text{STOP}$$

- In terms of traces the processes $X \parallel P$ and $Y \parallel P$ are the same:
 $\text{traces}(X \parallel P) = \text{traces}(Y \parallel P) = \{\varepsilon, a, ab\}$
- This trace equivalence however hides a difference in behaviour: $X \parallel P$ is always able to perform an a followed by a b , whereas $Y \parallel P$ can deadlock before any action is performed (depending on the choice Y makes before synchronizing with P)
 - Traces are not enough to identify deadlocks
- Trace equivalence is simple, but in some cases it may not be fine enough (depending on the application domain)